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Karen Vertz	(Depositor's name)
<i>Karen Vertz</i>	(Signature)
1-28-04	(Date)

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/051,639	01/18/2002	Craig T. Salling	TI-32536	8729

TITLE OF INVENTION: ESD IMPROVEMENT BY A VERTICAL BIPOLAR TRANSISTOR WITH LOW BREAKDOWN VOLTAGE AND HIGH BETA

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1330	\$300	\$1630	04/12/2004

EXAMINER	ART UNIT	CLASS-SUBCLASS
LANDAU, MATTHEW C	2815	257-362000

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2. For printing on the patent front page, list (1) the names of up to 3 registered patent attorneys or agents OR, alternatively, (2) the name of a single firm (having as a member a registered attorney or agent) and the names of up to 2 registered patent attorneys or agents. If no name is listed, no name will be printed.

1 W. James Brady, III

2 Frederick J. Telecky, Jr.

3 \_\_\_\_\_

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Texas Instruments Incorporated

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Dallas, Texas

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(Authorized Signature)

*Gary C. Honeycutt*  
Gary C. Honeycutt, Reg. No. 20,250

(Date)

1-28-04

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**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re the Application of

Applicant Craig T. Salling, et al.

Docket Number: TI-32536

Serial No.: 10/051,639

Art Unit: 2815

Filed: 01/18/02

Examiner: Matthew C. Landau

For: ESD Improvement by a Vertical Bipolar Transistor with  
Low Breakdown Voltage and High Beta

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<b>NAME OF INVENTOR(S):</b> Craig T. Salling, et al.	
<b>TITLE OF INVENTION:</b> ESD Improvement by a Vertical Bipolar Transistor with Low Breakdown Voltage and High Beta	
<b>TI FILE NO.:</b> TI-32536	<b>DEPOSIT ACCT. NO.:</b> 20-0668
<b>FAXED:</b> 1-28-04 <b>DUE:</b> 4-12-04 <b>ATTY/SECY:</b> GCH/kjv	
<b>RECEIPT DATE &amp; SERIAL NO.:</b> <b>Serial No.:</b> 10/051,639 <b>Filing Date:</b> 01/18/02	

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